A Practical Fast Acting Control Scheme for Fuzzy Logic-Based Voltage Stabilisation Control

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Abstract: This paper presents a simplified control model for stabilising a load voltage using a switched reactor in parallel with a fixed capacitor of static VAR compensator. Two IGBT's are used to control the reactance of the switched reactor. A uniform pulse width modulation is used for controlling the two switches. The compensator has a simple control circuit and structure. A complete modelling and numerical simulation for the proposed systems are presented. A high speed Digital Signal Processor is used for implementing proportional-integral (PI) and fuzzy load voltage controllers. Experimental results indicate the superiority of fuzzy logic control over the conventional proportional-integral control method. Simulation results are reported and proved to be in good agreement with the relevant experimental results.

Keywords: VAR compensator, Reactive power control, Load voltage stabilisation, Fuzzy logic control.

1 Introduction

In recent years, the demand for controllable reactive power source has gone up mainly for efficient and reliable operation of ac electric power system [1]. VAR compensators should be controlled to provide rapid and continuous reactive power supports during static and dynamic power system operating conditions [2]. Advances in power electronics technology have allowed the development of various types of controlled electronics VAR compensator for power system applications [3-4].

There have been several papers published regarding the use of shunt compensating capacitors with series harmonic suppressor inductors in power factor correction and terminal voltage stabilisation of nonlinear loads. There are various static VAR compensator topologies based on forced commutated voltage source inverter [5-12]. In reference [6], scheme uses a filter reactor on the line side and self-controlled dc bus. The modulation index is controlled directly by the VAR calculator. The scheme allows the use of optimized PWM patterns for

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harmonic rejection and is suitable for slow varying VAR demand applications. In reference [7], the modulation index is fixed and the capacitor voltage is controlled so that the inverter output voltage matches in magnitude the line voltage. The capacitor voltage control is achieved through phase shifting of the inverter voltage with respect to the line voltage. In spite of its simplicity, the load angle control scheme does not allow very fast response to be achieved particularly at high power ratings. Thyristors controlled reactors are a source of harmonics distortion in power systems [7].

In reference [8], two filter arms are considered; one is tuned for third harmonic frequency and other is for the fifth one. These filters are used to eliminate the third and fifth current harmonics as well as reducing the terminal voltage distortion. Synchronous compensators, mechanically switched capacitors and inductor and saturated reactors have been applied for many years to control the system voltage. Also, thyristor controlled reactor (TCR) devices together with fixed capacitors (FC) or thyristor – switched capacitors (TSC) have been used. The rating of inductors and capacitor used in the TCR/FC or TCR/TSC scheme, in most cases, exceed the reactive power supplied or absorbed by the scheme. This leads to considerable expenses and large requirement for land on which to locate the compensator [9]. Steady–state characteristics are reported for open loop systems for switch reactor based static VAR compensators [10]. In reference [11], a static reactive power compensator is designed to be capable of supply both balanced and unbalanced reactive power demands. In reference [12], error-driven, error-scaled, self-adjusting SVC-control strategy is driven by the presence of a nonlinear dynamic loads. This control ensures adequate compensation level and damping effectiveness at all times based on the magnitude of the excursion error deviation-vector in the error hyperplane.

In this paper, an efficient switched reactor in parallel with a capacitor for static VAR compensator is presented. To overcome the low–order harmonics and slow response associated with the conventional thyristor controlled reactor compensators, the uniform pulse width modulation is used for controlling the reactance of the switched reactor. The proposed system is implemented using a high speed digital signal processor (DSP) for PI and/or fuzzy logic load voltage controllers. The system behaviour under steady-state and transient condition is obtained and proved the superiority of the fuzzy logic controller over the conventional PI controller method. There are agreement between the simulation and experimental results.

2 Description of the System

The system under consideration is illustrated in Fig. 1. The system consists of two IGBT switches, one reactor, a fixed capacitor, and load; all of them are in parallel. However, two self-commutated bi-direction IGBT switches are used to

control the reactance of the switched reactor. The simulation and experimental results are verified using an inductive load and /or dynamic load. The source reactance is taken 15 % to 20 % of the load impedance and the source resistance is taken 10 % of the source reactance [8]. The values of the controlled reactance and capacitance of the compensator are selected according to the following relationship [8]:

$$\omega C = \frac{1}{\omega L_\theta} = \frac{1}{z_L} \sin \phi_1 . \tag{1}$$

The parameter values of the designed system are given in the Appendix. The differential equations that describe the mode of operation for this system are given in reference [10]. The gate pulses are achieved by comparing the controller output with the triangular carrier signal using the DSP. The operation principle of the proposed system can be illustrated in Fig. 1. The two switches Q_1 and Q_2 represent two self–commutated bi-directional switches. Switch Q_2 is used to freewheel the controlled inductor current ($i_{L\theta}$) when Q_1 is off. Through high frequency switching, the fundamental component of the inductor current ($i_{L\theta}$) can be controlled by controlling the output, to stabilise the load voltage at constant value.
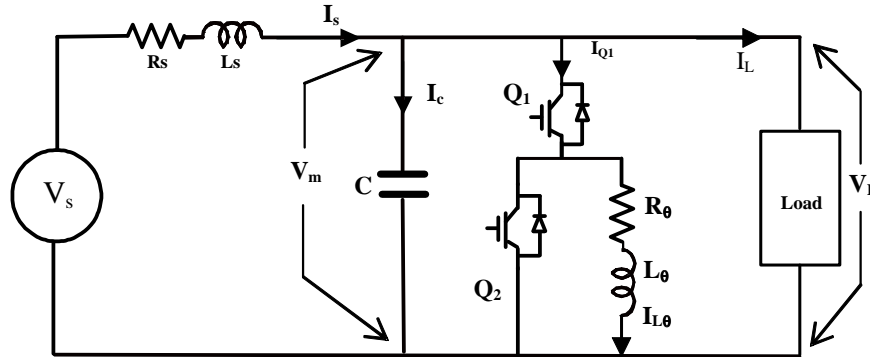

Fig. 1 – *Schematic diagram of proposed system.*

3   Fuzzy Controller for Load Voltage Stabilisation

The inputs of the fuzzy controller are the error e(k) and the change of error ce(k), which are defined as:

$$e\ (k) = V_{ref} - V_m\ (k), \tag{2}$$

$$ce\ (k) = e\ (k) - e\ (k\text{-}1), \tag{3}$$

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where V_m (k) is the present load voltage, V_{ref} is the reference voltage, and the subscript k denotes values taken at the beginning of the kth switching cycle. The output of the fuzzy controller is the control voltage and is defined as:

$$7V_c (k) = V_c (k-1) + \xi\Delta V_c (k), \tag{4}$$

where ΔV_c (k) is the inferred change of control voltage by the fuzzy controller at the kth sampling time, and ξ is the gain factor of the fuzzy controller. Adjusting ξ can change the effective gain of the controller.

The values of e(k), ce(k) and $\Delta V_c(k)$ are normalized. The triangular shape of the membership functions of this arrangement presumes that for any particular input there is only one dominant fuzzy subset. Also, for any combination of e and ce, a maximum of four rules are adopted.

The derivation of the fuzzy control rules is heuristic in nature and based on the following criteria:

1. when the load voltage of the common coupling bus-bar is far from the set point, the change of control signal must be large so as to bring the output to the set point quickly;

2. when the load voltage of the common coupling bus-bar is approaching the set point, a small change of control signal is necessary.

3. when the load voltage of the common coupling bus-bar is near the set point and is approaching it rapidly, the control signal must be kept constant so as to reduce overshoot;

4. when the set point is reached and the output input still changing, the control voltage must be changed a little bit to prevent the load voltage from moving away;

5. when the set point is reached and the load voltage is steady, the control voltage remains unchanged;

6. when the load voltage is above the set-point, the sign of the change of control voltage must be negative; and

7. when the load voltage is below the set-point, the sign of the change of control voltage must be positive.

The inference result of each rule consists of two parts, the compatibility (weighting factor) w_i of the individual rule, and the degree of change of control signal C_i according to the rule. The weighting factor w_i is obtained by applying the minimum operation on $\mu(e_0)$ and $\mu(ce_0)$, where e_0 and ce_0 are the singleton inputs of e and ce. C_i is obtained from the rules, which show the mapping from the product space of e and ce to C_i. The inferred output of each rule can therefore be written as:

$$\Delta Vci = \min \{\mu_c (e_0), \mu_{ce} (ce_0)\} C_i = w_i C_i, \tag{5}$$

where ΔV_{Ci} denotes the change of control signal inferred by the ith rule. After collecting all the results, a crisp value of the change of control voltage can be obtained. Here the method of the center of gravity is preferred. The resultant change of control signal can be found as:

$$\Delta V_c (k) = \frac{\sum_{i=1}^{N} \Delta V_{ci}}{\sum_{i=1}^{N} w_i} = \frac{\sum_{i=1}^{N} w_i\, C_i}{\sum_{i=1}^{N} w_i}, \tag{6}$$

where N is the maximum number of effective rules.

4 Simulation and Experimental Results

The proposed system is designed and implemented using DSP to verify the load voltage control requirements with PI controller and/or fuzzy logic controller. The experimental set-up is shown in Fig. 2, the load is static and/or dynamic. A universal motor under test is coupled to a self-excited dc generator, acting as a load. An incremental encoder is coupled to the motor shaft to measure the motor speed. The load, supply, and compensator current are sensed using hall-effect transducers, which have good linear response over wide range. Also, the load and supply voltages are sensed using voltage transducer, which has good linear response over wide range. This system is fully controlled by using dSPACE (DS1102) controller board, which is installed on a PC computer.

The behaviour of this system under steady state and transient conditions are obtained for static inductive load. The system has been tested using dynamic load, which is the universal motor. These results show the accuracy of the developed simulation when compared with the experimental results.
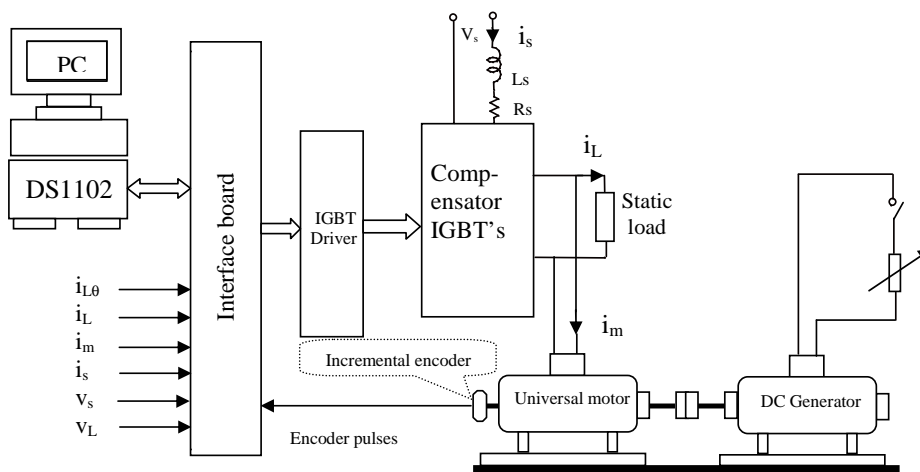

Fig. 2 – *Experimental set-up for DSP-based control.*

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4.1 Open –loop PI controller for static load

Fig. 3 shows the experimental waveforms of the supply voltage (v_s) and current (i_s) with duty ratio equal to 0.7. It is observed that the power factor at common coupling bus-bar near unity power factor. Also, it is clear that the supply voltage and current waveforms are sinusoidal.
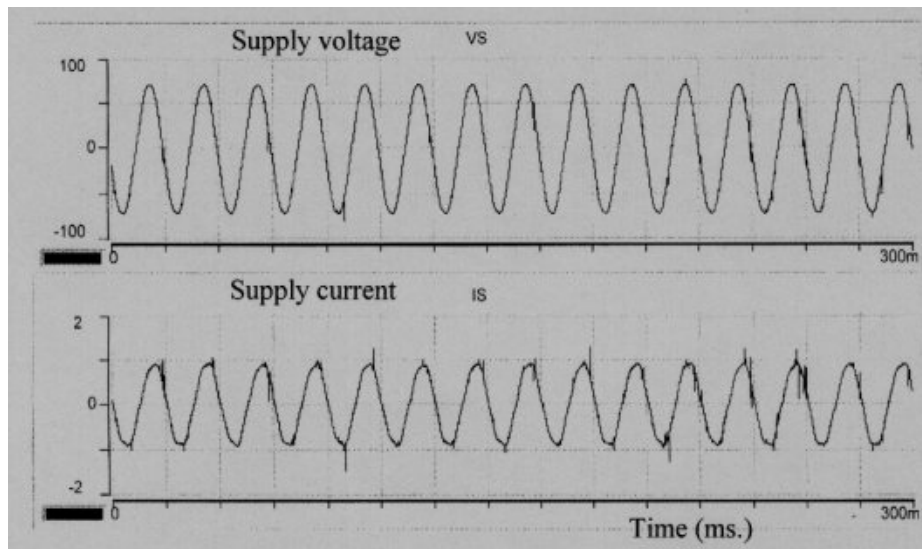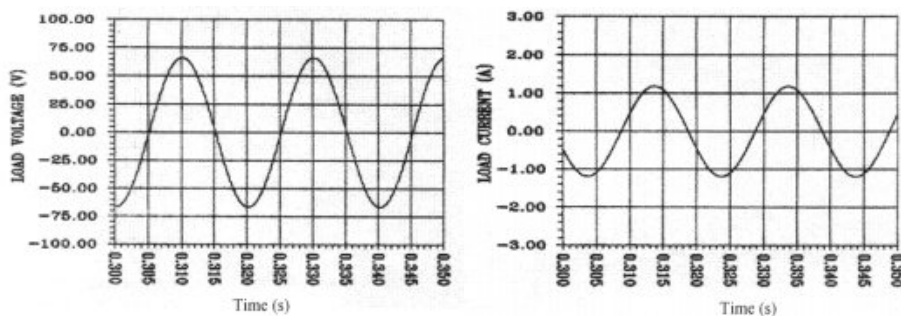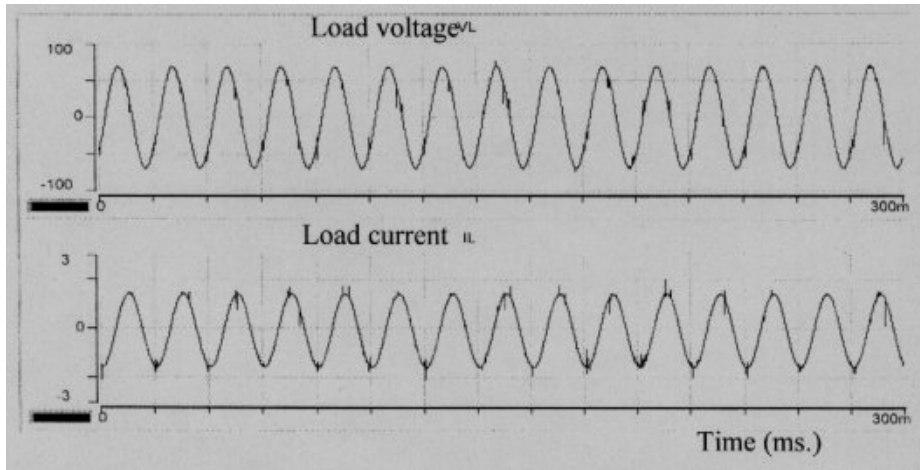

Fig. 3 – *Experimental waveforms for supply voltage and current.*

Fig. 4 shows the simulation and experimental waveforms of the load voltage (v_L) and current (i_L). It is noticed that the waveforms are sinusoidal. Fig. 5 shows the experimental load voltage (v_L), compensator inductor current ($I_{L\square}$) and switch Q_1 current (i_{Q1}). It is observed that the inductor current lags the common coupling load voltage by 90°.

(a) Simulation

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(b) Experimental

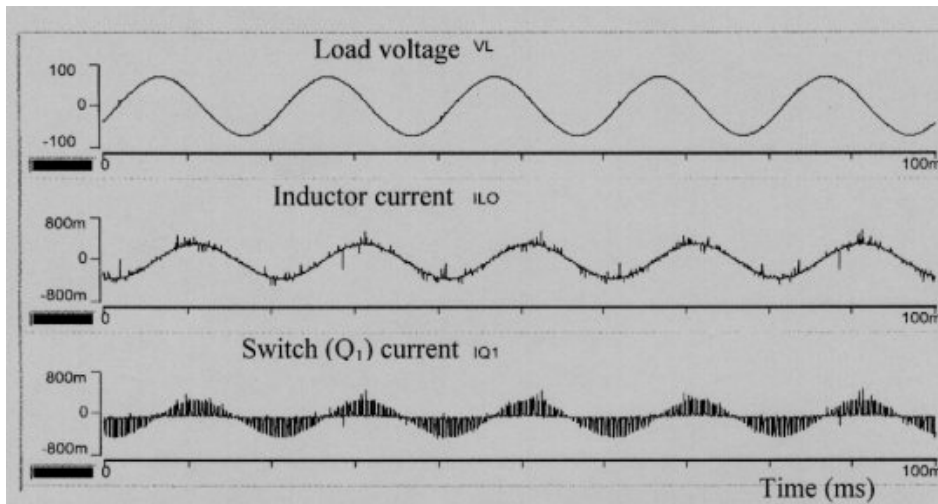**Fig. 4** – *Simulation and experimental results for load voltage and current.*

Fig. 5 – *Experimental waveforms for load voltage, inductor and switch current.*

4.2 Closed loop PI controller for static load

4.2.1 Reference voltage step change

Figs. 6 and 7 show the variation of the load rectified output voltage sensor and the load current due to positive and negative step change in reference voltage for $K_P = 5$ and $K_I = 0.1$.
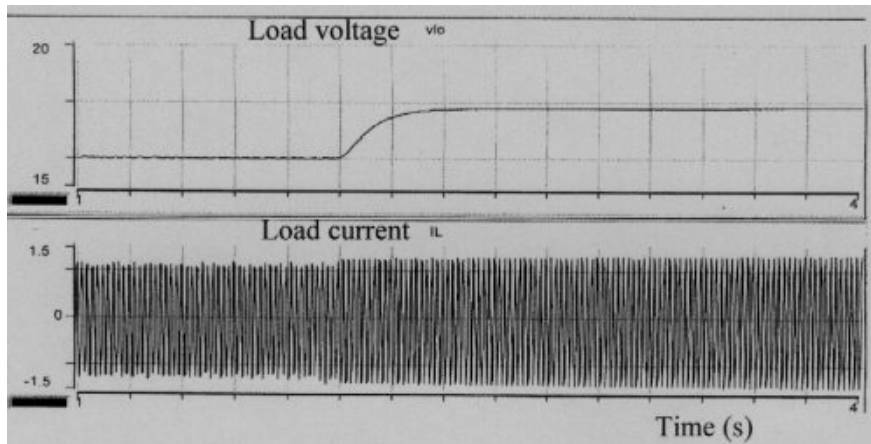
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Fig. 6 *– Variations of the load voltage and current*
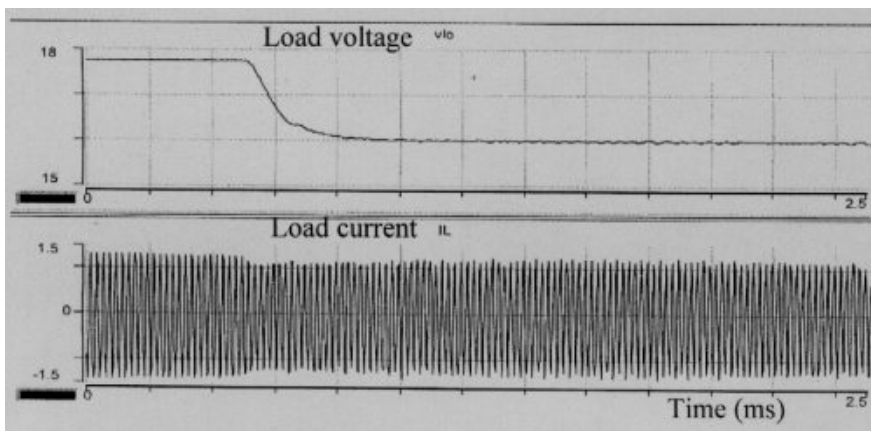*due to a positive step change in reference voltage.*

Fig. 7 *– Variations of the load voltage and current*
due to a negative step change in reference voltage

4.2.2 Load change

Fig. 8 shows the variations of the rectified load output voltage sensor, control voltage, and load current due to positive and negative load change (K_P=5, K_I=0.1).
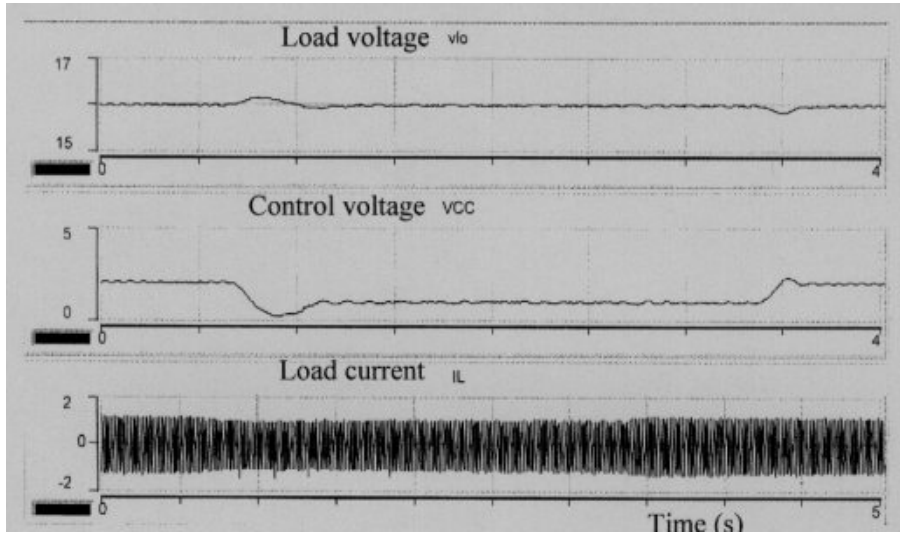
Fig. 8 – *Variations of load voltage and control voltage*
due to positive and negative load change

4.2.3 Open and closed loop run-up for the universal motor
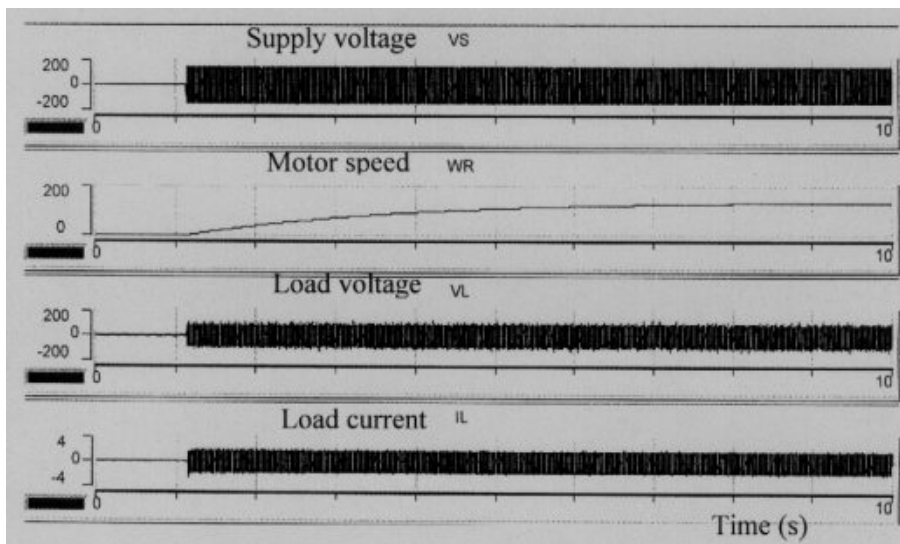

Fig. 9 – *Open loop run-up behaviour for half load.*

Figs. 9 and 10 show open and closed loop run-up behaviour of the universal motor as a dynamic load with the PI controller. It is observed that the run-up

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time for closed loop system is decreased and the common coupling bus bar voltage remains constant.
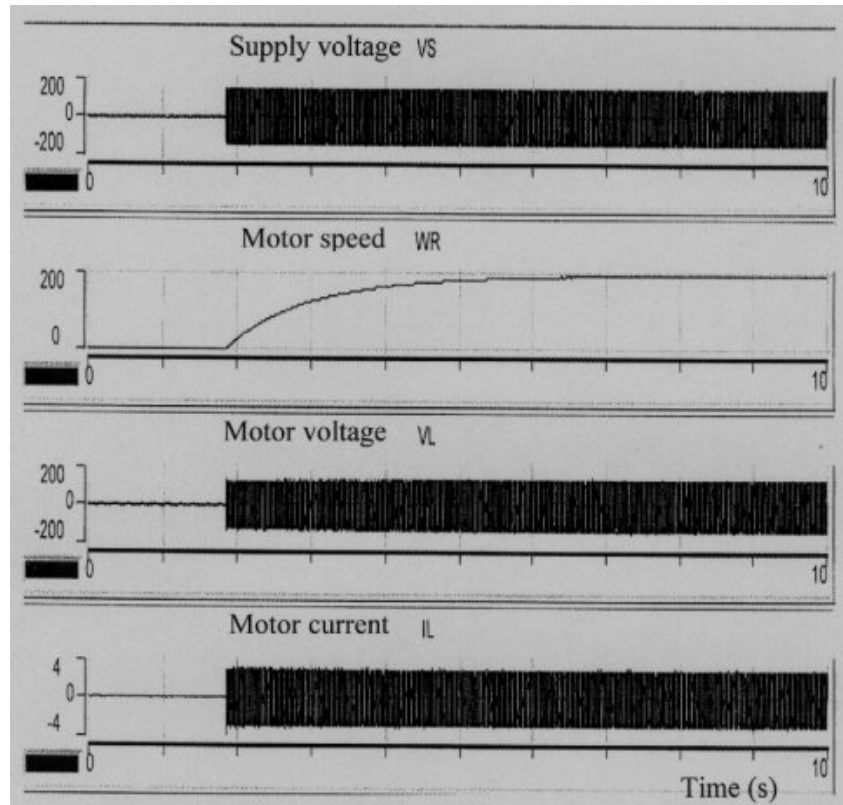

Fig. 10 – *Closed loop run-up behaviour for half load and* $V_{ref} = 140\,V$.

4.3 Closed loop fuzzy logic controller

The effectiveness of the proposed fuzzy logic controller on the transient and dynamic characteristics of the load voltage stabilisation is investigated.

4.3.1 Reference voltage step change for static load

Figs. 11 and 12 show the responses of the common coupling bus-bar rectified load output voltage sensor, and the control voltage due to positive and negative step change in the reference voltage for closed-loop fuzzy control system. It is noticed that the response is faster than the PI controller.
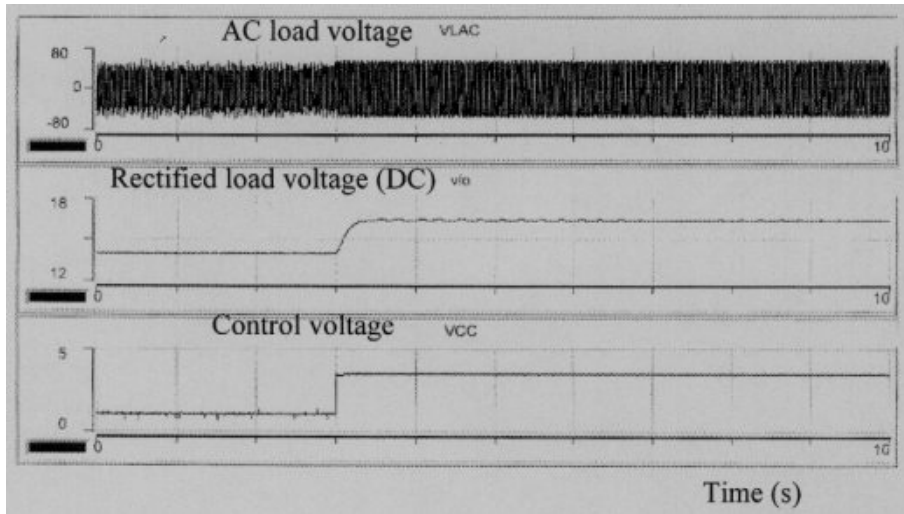
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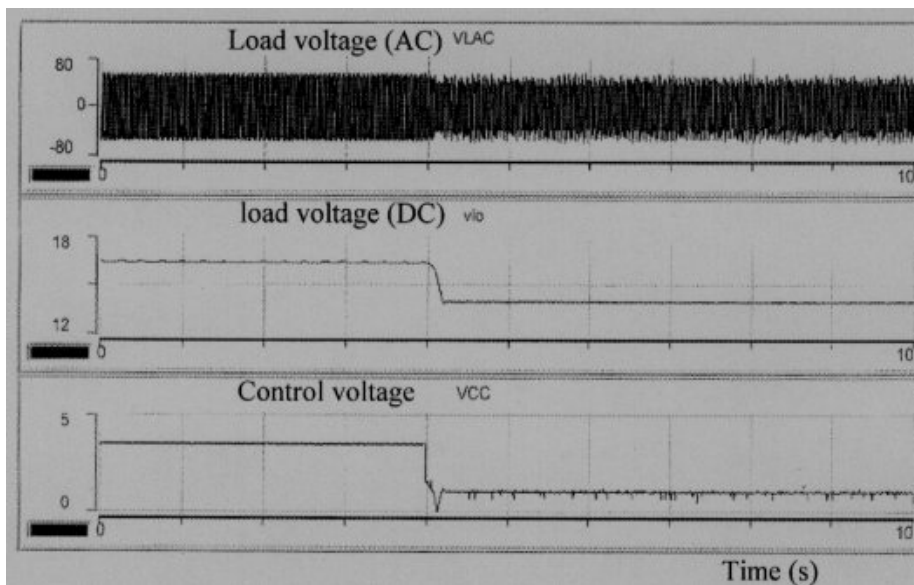**Fig. 11** – *Responses due to positive step change in reference voltage.*

Fig. 12 – *Responses due to negative step change in reference voltage.*

4.3.2 Static load change

Figs. 13 shows the response of the common coupling bus-bar rectified load voltage sensor, load current, and control voltage due to positive and negative

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load change for closed-loop voltage stabilisation using fuzzy control system. It is clear that the response with fuzzy control is faster than with the PI controller and the load voltage is constant.
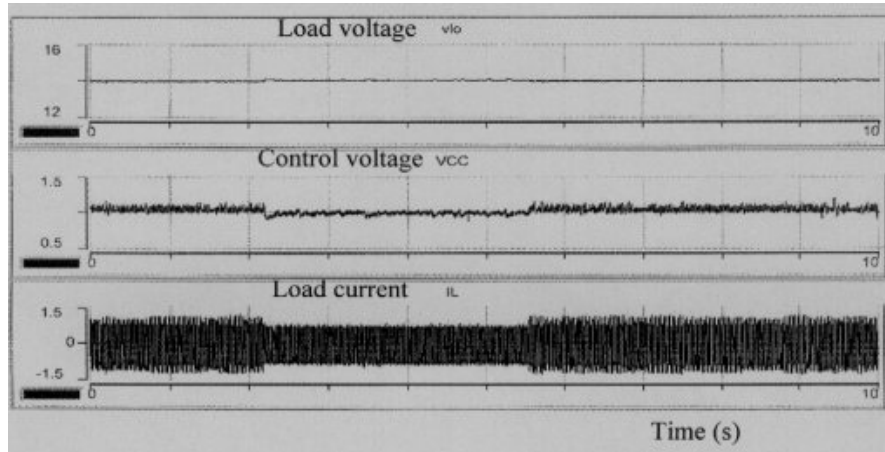

Fig. 13 – *Variations of load current, load voltage and control voltage*
due to positive and negative load change for closed loop fuzzy control.

4.3.3 Motor run-up using fuzzy control

Fig. 14 shows the waveforms of the motor current, the motor voltage and motor speed during run-up at half-load and $V_{ref} = 120$ V for closed loop voltage stabilisation using fuzzy logic control. It is observed that the response time is faster than when using the PI controller.
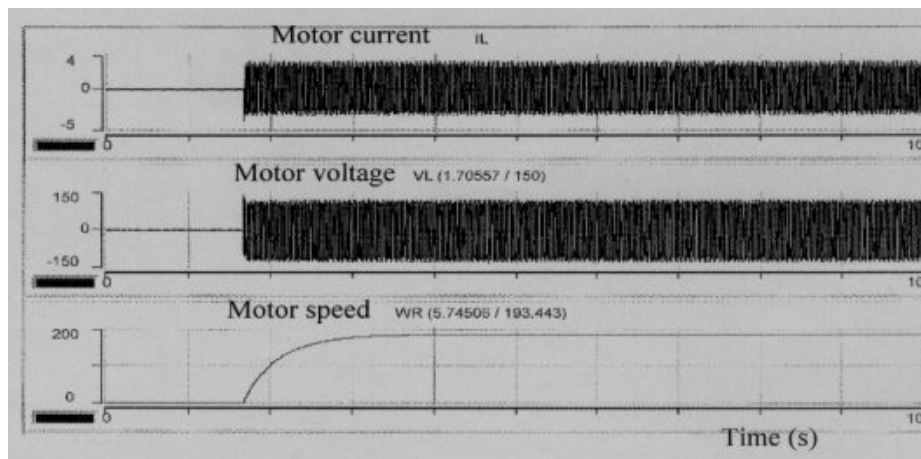

Fig. 14 – *Run-up behaviour for half-load for closed loop*
voltage stabilisation using fuzzy control.

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4.3.4 Dynamic load change

Figs. 15 and 16 show the motor voltage and speed response due to positive and negative change in load torque with V_{ref} = 120 V using fuzzy control. It is clear that the load voltage is constant and the time response is faster than when using the PI controller.
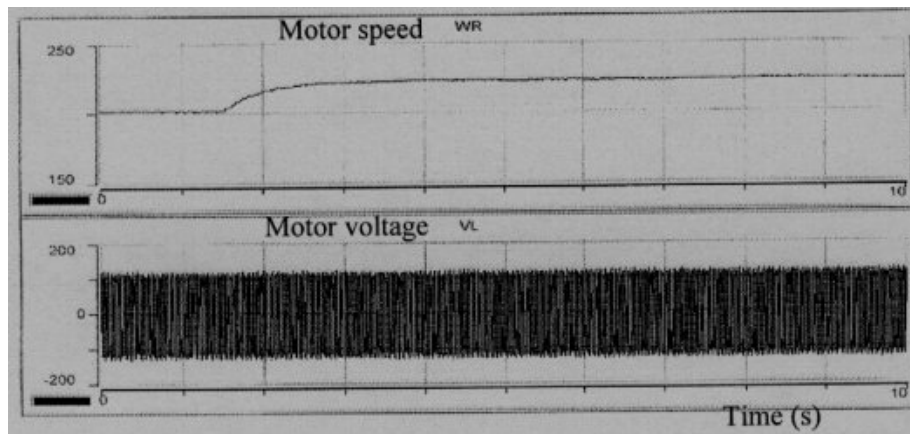

Fig. 15 – *Response due to a negative change in load torque using fuzzy control.*
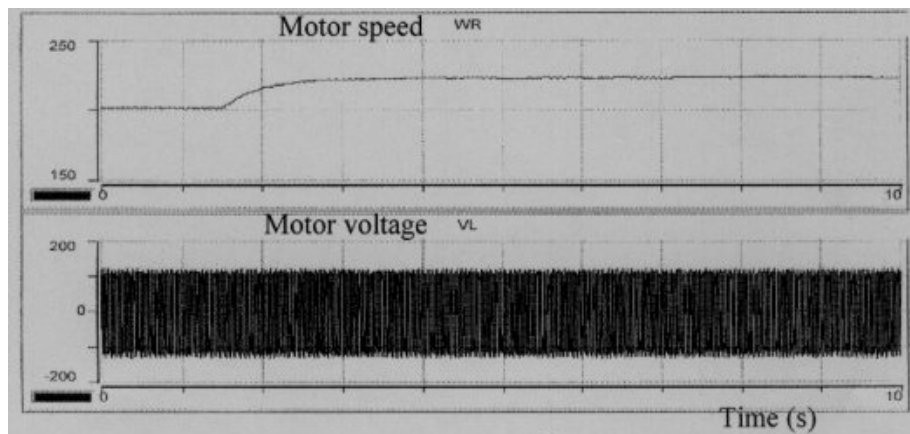

Fig. 16 – *Response due to a positive change in load torque using fuzzy control.*

5   Conclusion

A simplified PWM control technique is proposed with switched reactor in parallel with a capacitor for static VAR compensation. The proposed technique is for load voltage stabilisation, reactive power compensation, and power factor

improvement in the presence of feeder impedance. Appropriate parameters of the designed system are selected. The load terminal voltage, the load current and the supply current are shown to be close to the sinusoidal form.

The proposed system is fully controlled using the digital signal processor board. The proportional - integral voltage controller parameters are given to stabilise the common coupling bus-bar voltage. Moreover, the experimental study using DSP clearly indicates the superior performance of fuzzy logic control for static and or dynamic load. This is because it is inherently adaptive in nature. By good choice of control parameters, the fuzzy controller can give better results than the PI controller. Fuzzy control of the complete system gives good response in both the motor run-up and during disturbances.

6   Nomenclature

C:	Compensator capacitor, μF
F:	Motor viscous friction coefficient, Nm/r/s.
I_s, i_s:	R.M.S. and instantaneous supply current, A
L_s:	Supply inductance, H
L_θ, L_l:	Controlled coil and load inductances, H
R_θ, R_l:	Controlled coil and load resistances, Ω
R_s:	Supply resistance, Ω
V_s, v_s:	R.M.S. and instantaneous supply voltage, V
V_m, v_m:	R.M.S. and instantaneous load terminal voltage, V
Z_L:	Load impedance, Ω
ϕ_l:	Load phase- angle
ω:	Angular frequency
L_a, L_f:	The armature and field of the motor inductances, H
R_a, R_f:	The armature and field of the motor resistances, Ω
J:	The moment of inertia, kgm^2
T_e, T_l:	The electromagnetic and the load torque, Nm
K_P:	Proportional gain
K_I:	Integral gain

7 Appendix

The parameter values of the designed system at 50 Hz are as follows:

$R_l = 25\ \Omega$ $L_l = 0.159\ H$

$R_\theta = 2.5\ \Omega$ $L_\theta = 0.199\ H$

$R_s = 2.5\ \Omega$ $L_s = 0.027\ H$

$C = 55\ \mu F$

The test motor data and parameters:

1/3 HP, 220 V, 1.9 A, 6000 r/min

Universal motor has the following measured parameters:

$R_a + R_f = 13.7\ \Omega,$ $L_a + L_f = 0.169\ H,$

$F = 0.00012\ Nm/r/s,$

$J = 0.003\ kgm^2$

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